

## Explore64 I/O Pin Allocations.

	Pin Nbr		Pin Nbr
SSD1963 D5 - ANA	1		64 ANA - SSD1963 D4
SSD1963 D6 - ANA	2		63 5V - SSD1963 D3 - PWM1C
SSD1963 D7 - ANA	3		62 ANA - SSD1963 D2
SPI2 CLOCK - ANA	4		61 5V - SSD1963 D1
SPI2 OUT - ANA	5		60 5V - SSD1963 D0
CONSOLE Rx	6		59 5V - COM1 Rx
CPU Reset when Low			58 CONSOLE Tx
SPI1 OUT - ANA	8		55 5V - KEYBOARD DATA
COM2 Tx - ANA	11		54 5V - KEYBOARD CLOCK
	12		53 5V - PWM 2A
COM2 Rx - ANA	13		52 5V - COUNT
	14		51 ANA - COUNT - WU - IR
COM1 Tx - ANA	15		50 ANA - SPI1 CLOCK
COM3 Tx - ANA	16		49 ANA - COUNT
COM3 Rx - ANA	17		48 PWM 1A
	18		47 SPI2 IN - PWM 2B
	21		46 5V
	22		45 5V - SPI1 IN
COUNT - ANA	23		44 5V - I <sup>2</sup> C CLOCK
SSD1963 WR - ANA	24		43 5V - I <sup>2</sup> C DATA
SSD1963 RS - ANA	27		42 5V - PWM 1B
SSD1963 Reset - ANA	28		
COM1 Enable - ANA	29		
	30		3.3V OUTPUT (100mA MAX)
	31		5V OUTPUT OR INPUT
	32		GROUND
DIGITAL INPUT ONLY	33		



### Notes:

- All pins are capable of digital input/output and can be used as an interrupt pin.
- ANA means that the pin can be used as an analogue input.
- 5V means that the pin is 5V tolerant.
- SSD1963 refers to pins that are used to interface to LCD panels using the SSD1963 controller.
- If the serial console is disabled the CONSOLE pins can be used for COM4