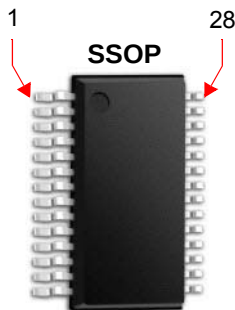
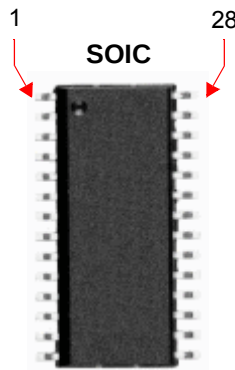
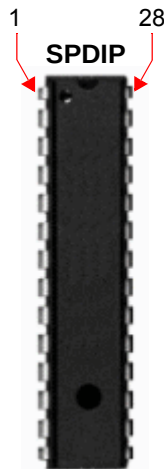


PIC32MX1XX/2XX 28/36/44-PIN FAMILY

Pin Diagrams

TABLE 3: PIN NAMES FOR 28-PIN GENERAL PURPOSE DEVICES

28-PIN SOIC, SPDIP, SSOP (TOP VIEW) ^(1,2,3)	
PIC32MX110F016B PIC32MX120F032B PIC32MX130F064B PIC32MX130F256B PIC32MX150F128B PIC32MX170F256B	
	
	
Pin #	Full Pin Name
1	MCLR
2	VREF+/CVREF+/AN0/C3INC/RPA0/CTED1/RA0
3	VREF-/CVREF-/AN1/RPA1/CTED2/RA1
4	PGED1/AN2/C1IND/C2INB/C3IND/RPB0/RB0
5	PGEC1/AN3/C1INC/C2INA/RPB1/CTED12/RB1
6	AN4/C1INB/C2IND/RPB2/SDA2/CTED13/RB2
7	AN5/C1INA/C2INC/RTCC/RPB3/SCL2/RB3
8	Vss
9	OSC1/CLK1/RPA2/RA2
10	OSC2/CLKO/RPA3/PMA0/RA3
11	SOSCI/RPB4/RB4
12	SOSCO/RPA4/T1CK/CTED9/PMA1/RA4
13	VDD
14	PGED3/RPB5/PMD7/RB5
15	PGEC3/RPB6/PMD6/RB6
16	TDI/RPB7/CTED3/PMD5/INT0/RB7
17	TCK/RPB8/SCL1/CTED10/PMD4/RB8
18	TDO/RPB9/SDA1/CTED4/PMD3/RB9
19	Vss
20	VCAP
21	PGED2/RPB10/CTED11/PMD2/RB10
22	PGEC2/TMS/RPB11/PMD1/RB11
23	AN12/PMD0/RB12
24	AN11/RPB13/CTPLS/PMRD/RB13
25	CVREFOUT/AN10/C3INB/RPB14/SCK1/CTED5/PMWR/RB14
26	AN9/C3INA/RPB15/SCK2/CTED6/PMCS1/RB15
27	AVss
28	AVDD

- Note** 1: The RPN pins can be used by remappable peripherals. See [Table 1](#) for the available peripherals and [Section 11.3 "Peripheral Pin Select"](#) for restrictions.
- 2: Every I/O port pin (RAX-RCx) can be used as a change notification pin (CNAX-CNCx). See [Section 11.0 "I/O Ports"](#) for more information.
- 3: Shaded pins are 5V tolerant.