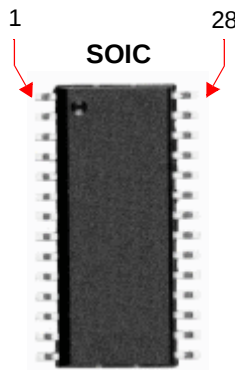


PIC32MX1XX/2XX 28/44-PIN XLP FAMILY

TABLE 4: PIN NAMES FOR 28-PIN GENERAL PURPOSE DEVICES WITHOUT VBAT

28-PIN SOIC (TOP VIEW)^(1,2,3) PIC32MX154F128B PIC32MX174F256B			
Pin #	Full Pin Name	Pin #	Full Pin Name
1	MCLR	15	PGEC3/RPB6/ASCL2/PMD6/RB6
2	VREF+/AN0/C3INC/RPA0/ASDA1/CTED1/PMA1/RA0	16	TDI/RPB7/CTED3/PMD5/INT0/RB7
3	VREF-/AN1/RPA1/ASCL1/CTED2/RA1	17	TCK/RPB8/SCL1/CTED10/PMD4/RB8
4	PGED2/AN2/C1IND/C2INB/C3IND/RPB0/RB0	18	TDO/RPB9/SDA1/CTED4/PMD3/RB9
5	PGEC2/AN3/C1INC/C2INA/LVDIN/RPB1/CTED12//RB1	19	Vss
6	AN4/C1INB/C2IND/RPB2/SDA2/CTED13/RB2	20	VCAP
7	AN5/C1INA/C2INC/RTCC/RPB3/SCL2/RB3	21	PGED1/RPB10/CTED11/PMD2/RB10
8	Vss	22	PGEC1/TMS/RPB11/PMD1/RB11
9	OSC1/CLKI/RPA2/RA2	23	AN12/PMD0/RB12
10	OSC2/CLKO/RPA3/PMA0/RA3	24	AN11/RPB13/CTPLS/PMRD/RB13
11	SOSCI/RPB4/RB4 ⁽⁴⁾	25	CVREFOUT/AN10/C3INB/RPB14/SCK1/CTED5/PMWR/RB14
12	SOSCO/RPA4/T1CK/CTED9/RA4	26	AN9/C3INA/RPB15/SCK2/CTED6/PMCS1/RB15
13	VDD	27	AVss
14	PGED3/RPB5/ASDA2/PMD7/RB5	28	AVDD

- Note**
- 1: The RPn pins can be used by remappable peripherals. See [Table 1](#) for the available peripherals and [12.3 "Peripheral Pin Select"](#) for restrictions.
 - 2: Every I/O port pin (RAX-RBx) can be used as a change notification pin (CNAX-CNBx). See [12.0 "I/O Ports"](#) for more information.
 - 3: Shaded pins are 5V tolerant.
 - 4: This is an input-only pin.